500.35459VV2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

T. Sekiguchi, et al.

Filed:

March 29, 2004

For:

METHOD OF MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICES HAVING A MEMORY
DEVICE WITH REDUCED BIT LINE STRAY CAPACITY AND
SUCH SEMICONDUCTOR INTEGRATED CIRCUIT DEVICES

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97 AND § 1.98

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 March 29, 2004

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with prior applications of the above-identified application, that is, Application No. 10/226,290, filed August 23, 2002, and Application No. 08/862,320, filed May 23, 1997.

Since each of No. 10/226,290 and No. 08/862,320 is being relied upon in the above-identified application under 35 USC § 120, copies of the listed documents are not enclosed. See 37 CFR 1.98(d).

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified application. Clearly, requirements of 37 CFR § 1.97(b) are satisfied.

In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR § 1.97 and § 1.98 have been satisfied, in connection with all

documents on the enclosed List. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 500.35459VV2) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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	AA		9,560	2/1995	Park						Date	
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	AC		9,948	10/1996	Kim						1.	
	AD	5,677,227		10/1997	Yang, et al.							
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.